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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 2, 2007
Date

Jennifer A. Steele
Jennifer A. Steele

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ronnie M. Harrison

Attorney Docket No.: 500800.04

Patent No. : US 6,911,807 B2

Serial No. : 10/823,301

Issue Date : June 28, 2005

Filed : April 12, 2004

Title : METHOD AND CIRCUIT FOR LIMITING A PUMPED VOLTAGE

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
FEB 12 2007
of Correction

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (56), References Cited, Other Publications, Gregorian Reference	"1986. pp. 146-156."	--1986, pp. 146-156.--
Column 2, Lines 39-43	"When the overvoltage signal OV as a sufficient magnitude, the transistor 130 turns ON allowing current to flow through the diode-coupled	--When the overvoltage signal OV has a sufficient magnitude, the transistor 130 turns ON allowing current to flow through the diode-coupled transistor 132 and

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	transistor 132 and transistor to the supply voltage VCC to thereby clamp the pumped voltage VCCP."	transistor 130 to couple the output of the charge pump 106 to the supply voltage VCC to thereby clamp the pumped voltage VCCP.--
Column 6, Line 4	"VREF to and thereby control"	--VREF to thereby control--
Column 6, Line 15	"operate combination to"	--operate in combination--
Column 6, Line 38	"reference voltage VREF the"	--reference voltage VREF, the--
Column 6, Line 57	"combination to the to limit the value"	--combination to limit the value--
Column 8, Line 8	"may be placed integrated"	--may be placed in integrated--
Column 8, Line 21	"One or more data shortage"	--One or more data storage--
Column 10, Line 48	"a value that s function"	--a value that is a function--
Column 10, Line 64	"and decrease the current"	--and decreasing the current--
Column 11, Line 20	"output of charge pump"	--output of a charge pump--
Column 12, Line 10	"a value that s a function"	--a value that is a function--
Column 12, Line 52	"coupled to the output of charge pump"	--coupled to the output of the charge pump--

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent or in the original application. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.

FEB 13 2007

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date:

Jan. 31, 2007

By:

Edward W. Bulchis

Edward W. Bulchis, Reg. No. 26,847

Customer No. 27,076

Dorsey & Whitney LLP

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Seattle, WA 98101

(206) 903-8785

Attorney for Applicant(s)

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Enclosures:

Postcard

Form PTO-1050 (+ copy)

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : US 6,911,807 B2
DATED : June 28, 2005
INVENTOR(S) : Ronnie M. Harrison

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MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101

Patent No. US 6,911,807 B2

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